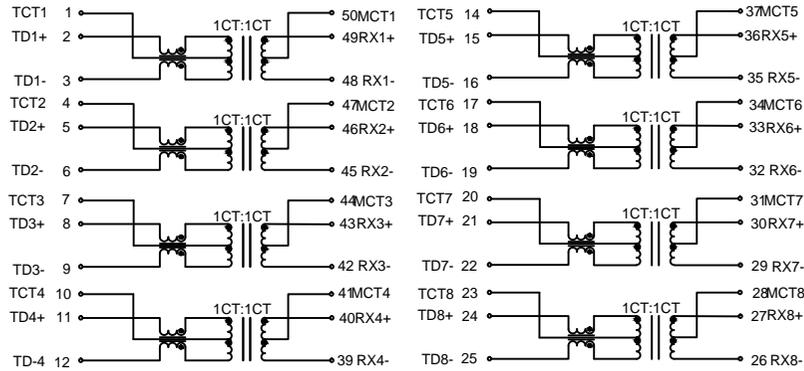
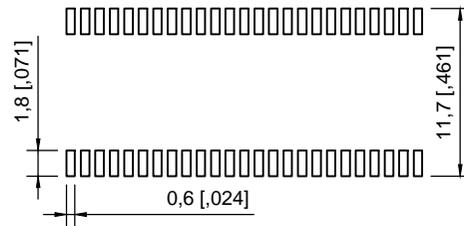
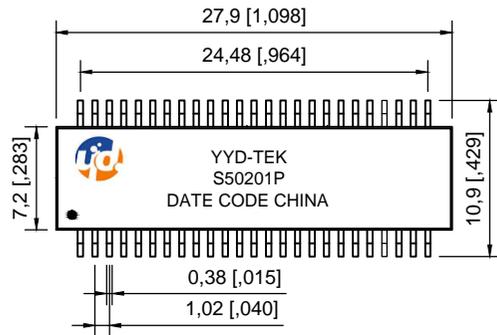


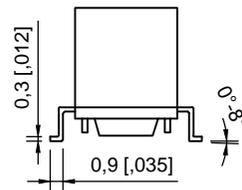
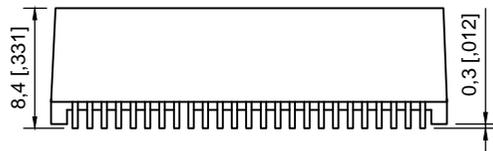
Schematic



Mechanical



SUGGESTED PADLAYOUT



版次	修改说明	日期	设计
A0	NEW	20220629	JLLI

ELECTRICAL SPECIFICATONS @25°C

PARAMETER	SPECIFICATIONS
OPERATING TEMPERATURE	-0°C to +70°C
STORAGE TEMPERATURE	-25°C TO 125°C
INDUCTANCE(OCL)	350uH MIN @100KHz 100mV With 8mA DC Bias For (CHANNEL 1,2,3,4,5,6,7,8)
leakage inductance	0.5uH MAX
TURNS RATIO	1:1±5%
Cw/w	35pF MAX
D.C.R	1.2Ω MAX
INSERTION LOSS(IL)	1.0dB MAX@1MHz-100MHz
RETURN LOSS(RL) (Z out=100 OHM)	-18dB MIN@0.1MHz-30MHz;-16.1dB MIN@30-60MHz -12dB MIN@60MHz-80MHz;-10dB MIN@100MHz
CROSSTALK	-30dB MIN@1MHz-100MHz;
DCMR	-40dB MIN@30MHz;-35dB MIN@60MHz -30dB MIN@100MHz;
HI-POT	Isolation HI-POT:1500Vrms/1mA/60S DISI Test 4000VDC
STANDARD	Designed to meet IEEE802.3ab&802.3bt Requiement 720mA Current capability POE++ 60W

深圳市毅远达电子有限公司
Shenzhen Yiyuanda Electronics Co., LTD

自由公差:	
长度尺寸	角度尺寸
0. XX X°	±
X. XX ±0.25	0. X° ±
XX. XX ±0.25	

型号: 50 PIN SMD
PROFILE PACKAGE

料号:

品名: S50201P

比例

1:1

单位

mm

角法



核准:

校对: JLLI

设计: JLLI

部门:

日期: 20220630

版本: A0