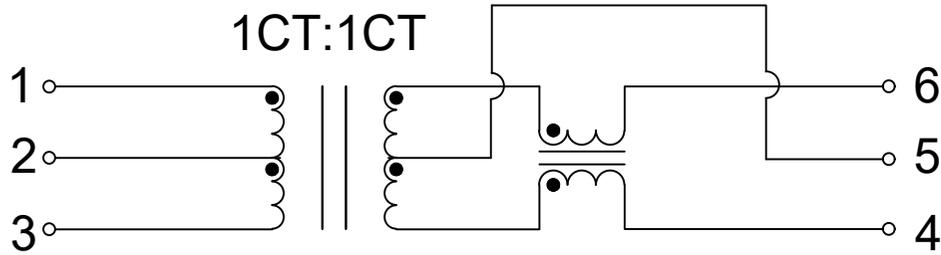


NO.	REV	DESCRIPTION.	APPD.	Date
1	A0	NEW	CAO	2024/04/17

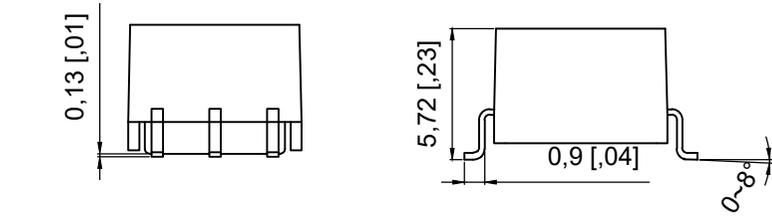
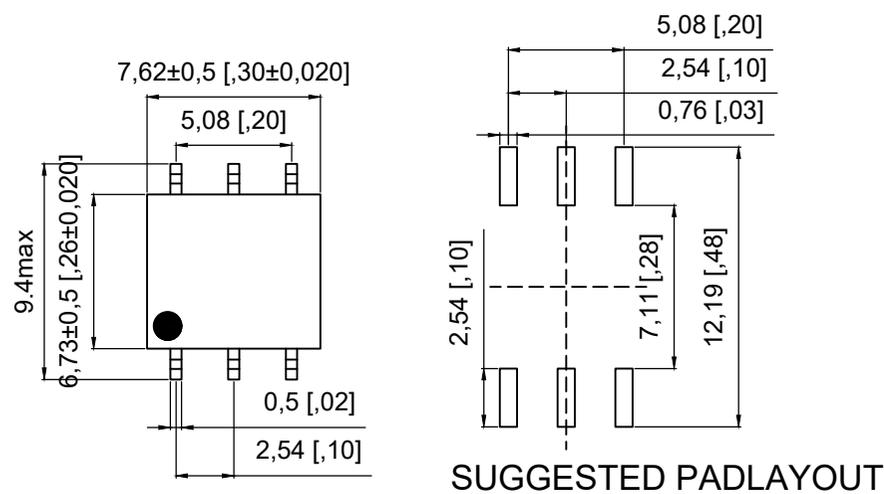
Schematic:



ELECTRICAL SPECIFICATONS@25° C

PARAMETER	SPECIFICATIONS	
QUALIFICATION	PER AEC-Q200	
DESIGN CONSTRUCTION	FUNCTIONAL INSULATION	
CREEPAGE DISTANCE	>5mm	
OPERATING TEMPERATURE	-40°C TO 125°C	
TURNS RATIO	1:1±2%	
POLARITY	PER SCHEMATIC	
DC RESISTANCE	TRANSFORMER SIDE	CM CHOKE SIDE
	0.50 OHMS MAX	0.90 OHMS MAX
INSERTION LOSS(IL)	150uH MIN	
LEAKEAGE INDUCTANCE	0.5uH MAX@ 100KHz	
INSERTION LOSS	-0.30dB MAX 4MHz	
RETURN LOSS (Z OUT = 100 OHM±1%)	-20dB MIN @4MHz	
COMMON MODE REJECTION RATIO	1-10 MHz	10-100 MHz
	-30dB MIN	-12dB MIN
INPUT-OUTPUT ISOLATION	4300V DC FOR 60 SECONDS WITH 1.0 mA MAX OF LEAKAGE CURRENT	
INSULATION RESISTANCE	400 MEGAOHMS MINIMUM	
WORKING VOLTAGE	1000V DC	

Mechanical:



产品图				深圳市毅远达电子有限公司 Shenzhen Yi Yuan Da Electronic Co., LTD	
未注公差 TOLERANCE UNLESS OTHERWISE SPECIFIED	0. XX ±0.10	Description		6PIN SMD Designed to meet IEEE802.3u Requirement	
	X. XX ±0.25	比例 SCALE	1:1	版本 REV.	A0
	XX. XX ±0.25	制图 CHECKED	CAO	日期 DATE	2024-04-17
	X° ±2°	校对 CHECKED	CAO	日期 DATE	2024-04-17
张数 SHEET	3/3	大小 SIZE	A4	日期 DATE	2024-04-17
单位 UNIT	mm inch	角法 PROJ.	核准 APPROVED	日期 DATE	
				品名 TITLE	GH2103NL
				料号 PART NO.	
				图号 DWG NO.	

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