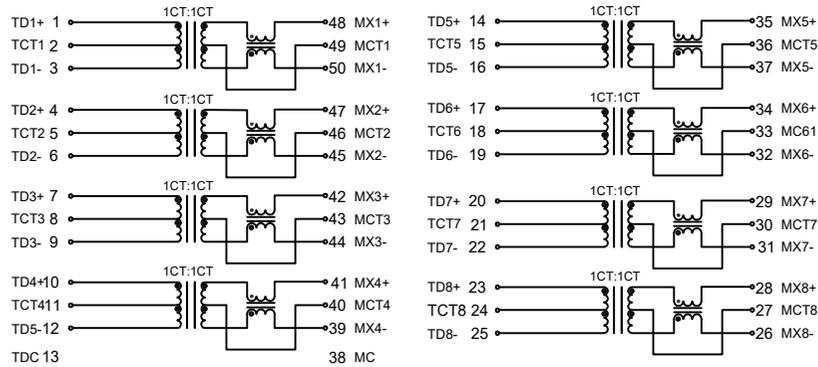
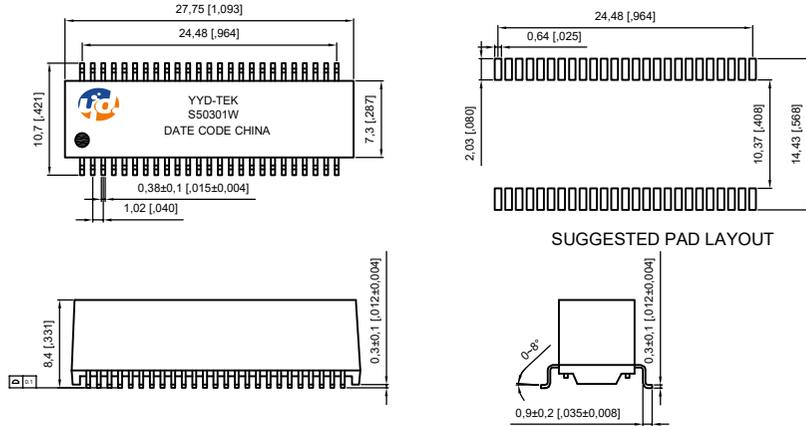


Schematic



Mechanical



SUGGESTED PAD LAYOUT

Units:mm[Inches] Tolerances:xx.x0±0.25[0.010]
0.xx±0.15[0.002]

版次	修改说明	日期	设计
A0	NEW	20220726	JLLI

ELECTRICAL SPECIFICATONS @25°C

PARAMETER	SPECIFICATIONS
OPERATING TEMPERATURE	-40°C to +85°C
STORAGE TEMPERATURE	-25°C TO 125°C
INDUCTANCE(OCL)	350uH MIN @100KHz 100mV With 13mA DC Bias For (CHANNEL 1,2,3,4)
leakage inductance	0.5uH MAX
TURNS RATIO	1:1±3%
Cw/w	30pF MAX
D.C.R	1.0Ω MAX
INSERTION LOSS(IL)	1.0dB MAX@0.3MHz-100MHz
RETURN LOSS(RL) (Z out=100 OHM)	-18dB MIN@1MHz-30MHz;-14dB MIN@30-60MHz -12dB MIN@60MHz-80MHz;-10dB MIN@100MHz
Cross Talk	-30dB MIN @1-100MHz;
CMRR	-30dB MIN @1-100MHz
HI-POT	1500Vrms/1mA/1S
STANDARD	Designed to meet IEEE802.3ab Requirement

深圳市毅远达电子有限公司
Shenzhen Yiyuanda Electronics Co., LTD

自由公差:	
长度尺寸	角度尺寸
0.XX X°	±
X.XX ±0.25	0.X° ±
XX.XX ±0.25	

型号: 50PIN SMD PROFILE PACKAGE

料号:

品名: S50301W

比例

1:1

单位

mm

角法



核准:

校对:JLLI

设计:JLLI

部门:

日期: 20220726

版本:A