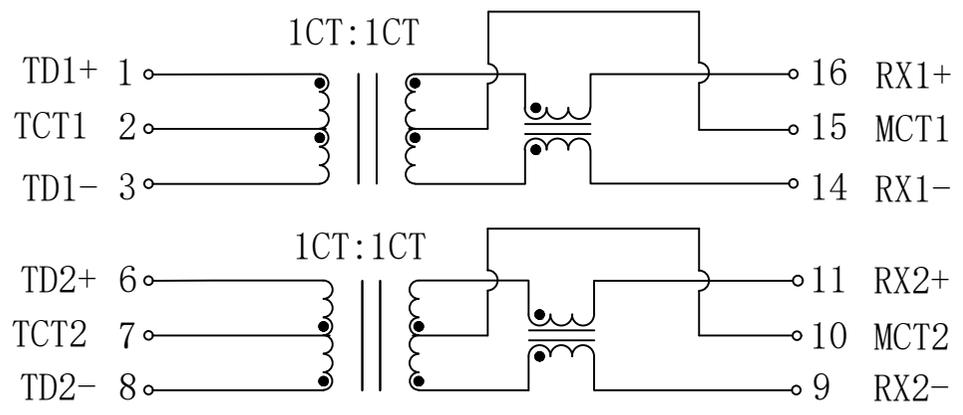


NO.	REV	DESCRIPTION.	APPD.	Date
1	1	NEW		2023/03/09

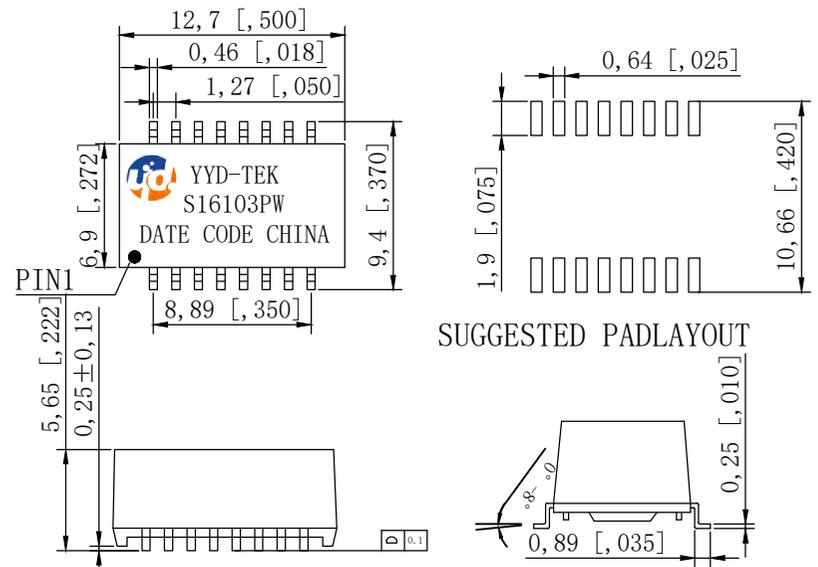
Schematic



ELECTRICAL SPECIFICATONS@25°C

PARAMETER	SPECIFICATIONS
OPERATING TEMPERATURE	-40°C to +85°C
STORAGE TEMPERATURE	-25°C TO 125°C
INDUCTANCE (OCL)	350uH MIN @100KHz 100mV With 8mA DC Bias For (CHANNEL 1, 2.)
leakage inductance	0.5uH MAX
URNS RATIO	1:1 ±2%
Cw/w	35pF MAX
D. C. R	(1-3), (6-8) 1.0Ω MAX; (16-14), (11-9) 1.2Ω MAX
INSERTION LOSS(IL)	-1.0dB MAX@1MHz-100MHz
RETURN LOSS(RL) (Z out=100 OHM)	-18dB MIN@1MHz-30MHz; -16dB MIN@30-60MHz -12dB MIN@60MHz-80MHz; -10dB MIN@100MHz
CROSSTALK	-30dB MIN @1-100MHz
DCMR	-40dB MIN @30MHz; -35dB MIN @30MHz; -30dB MIN @30MHz;
HI-POT	1500Vrms
STANDARD	Designed to meet IEEE802.3u Requirement POE++ applications cable side with720mA current capability

Mechanical



Dimension:mm
Unless otherwise specified,all tolerance are±0.25

产品图		深圳市毅远达电子有限公司 Shenzhen Yi Yuan Da Electronic Co., LTD	
未注公差 TOLERANCE UNLESS OTHERWISE SPECIFIED	0. XX ±0.10 X. XX ±0.25 XX. XX ±0.25 X° ±2°	Description	16PIN SMD Designed to meet IEEE802.3u Requirement POE
比例 SCALE	1:1	版本 REV.	A0
张数 SHEET	1/1	制图 CHECKED	CAO
单位 UNIT	mm inch	日期 DATE	2023-03-09
	角法 PORJ.	日期 DATE	2023-03-09
	核准 APPROVED	品名 TITLE	S16103PW
		料号 PART NO.	
		图号 DWG NO.	