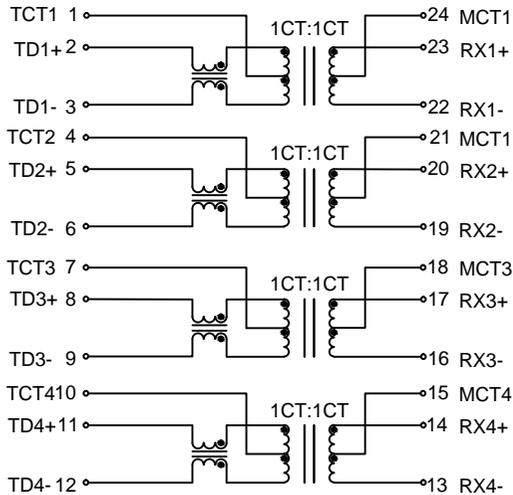
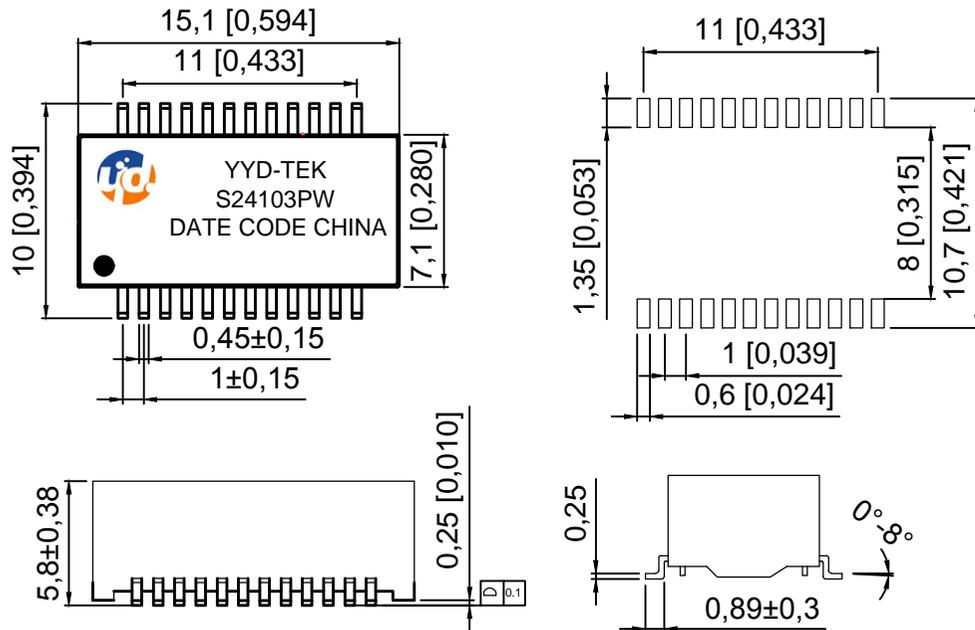


Schematic



Mechanical



版次	修改说明	日期	设计
A0	NEW	20220628	JLLI

ELECTRICAL SPECIFICATONS @25°C

PARAMETER	SPECIFICATIONS
OPERATING TEMPERATURE	-40°C to +85°C
STORAGE TEMPERATURE	-25°C TO 125°C
INDUCTANCE(OCL)	350uH MIN @100KHz 100mV With 8mA DC Bias For (CHANNEL 1,2,3,4)
leakage inductance	0.5uH MAX
TURNS RATIO	1:1±5%
Cw/w	35pF MAX
D.C.R	1.2Ω MAX
INSERTION LOSS(IL)	1.0dB MAX@1MHz-100MHz
RETURN LOSS(RL) (Z out=100 OHM)	-18dB MIN@1MHz-30MHz;-16dB MIN@30-60MHz -12dB MIN@60MHz-80MHz;-10dB MIN@100MHz
CROSSTALK	-30dB MIN @1-100MHz
DCMR	-30dB MIN @1-100MHz;
HI-POT	1500Vrms/1mA/1S
STANDARD	Designed to meet IEEE802.3at Requirement Suitable for End-span PoE application with 600mA current capability

深圳市毅远达电子有限公司
Shenzhen Yiyuanda Electronics Co., LTD

自由公差:	
长度尺寸	角度尺寸
0. XX X° ±	±
X. XX ±0.25	0. X° ±
XX. XX ±0.25	

型号: 24 PIN SMD PROFILE PACKAGE

料号:

品名: S24103PW

比例

1:1

单位

mm

角法



核准:

校对: JLLI

设计: JLLI

部门:

日期: 20220628

版本: A0