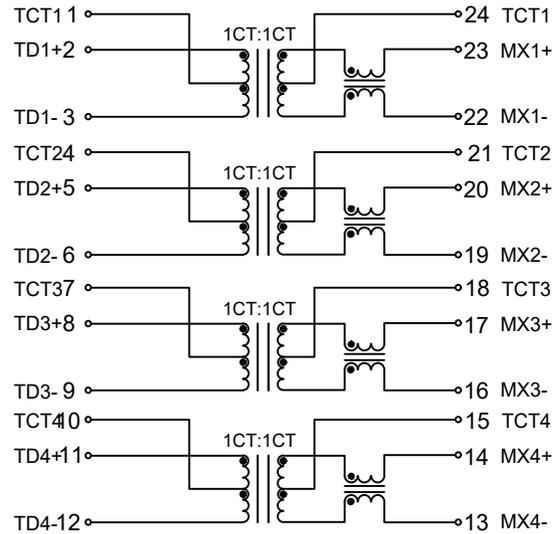
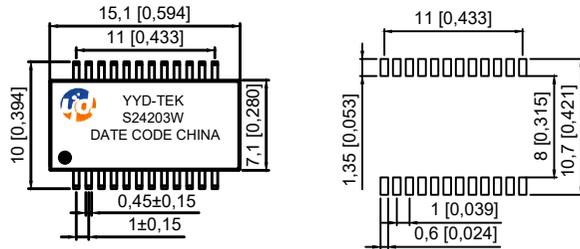


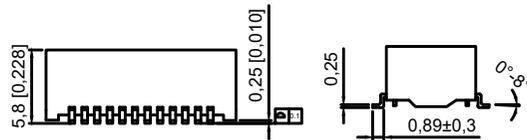
Schematic:



Mechanical:



SUGGESTED PAD LAYOUT



Units:mm[Inches] Tolerances:xx.x0±0.25[0.010]
0.xx±0.05[0.002]

NO.		REV	DESCRIPTION.	APPD.	Date
		A0	NEW	CAO	2023-11-22

ELECTRICAL SPECIFICATONS@25°C

PARAMETER	SPECIFICATIONS
OPERATING TEMPERATURE	-40°C to +85°C
STORAGE TEMPERATURE	-25°C TO 125°C
INDUCTANCE(OCL)	350uH MIN @100KHz 100mV With 8mA DC Bias For (CHANNEL 1,2,3,4)
leakage inductance	0.5uH MAX
URNS RATIO	1:1±3%
Cw/w	28pF MAX
D.C.R	1.2Ω MAX
INSERTION LOSS(IL)	-1.0dB MAX@1MHz-100MHz
RETURN LOSS(RL) (Z out=100 OHM)	-16dB MIN@1MHz-40MHz; -16+20LOG(f/40MHz)dB MIN@40MHz-100MHz
CROSSTALK	-40dB MIN @1MHz-30MHz;-35dB MIN @60MHz; -28dB MIN @100MHz;
DCMR	-40dB MIN @30MHz;-35dB MIN @60MHz; -30dB MIN @100MHz;
HI-POT	1500Vrms
STANDARD	Designed to meet IEEE802.3ab Requiement

产品图				深圳市毅远达电子有限公司 Shenzhen Yi Yuan Da Electronic Co., LTD					
未注公差 TOLERANCE UNLESS OTHERWISE SPECIFIED	0.XX ±0.10	X.XX ±0.25	XX.XX ±0.25	Description					
比例 SCALE	1:1	版本 REV.	A0	制图 CHECKED	CAO	日期 DATE	2023-11-22	品名 TITLE	S24203W
张数 SHEET		大小 SIZE	A4	校对 CHECKED		日期 DATE	2023-11-22	料号 PART NO.	
单位 UNIT	mm inch	角法 PORJ.		核准 APPROCED		日期 DATE		图号 DWG NO.	