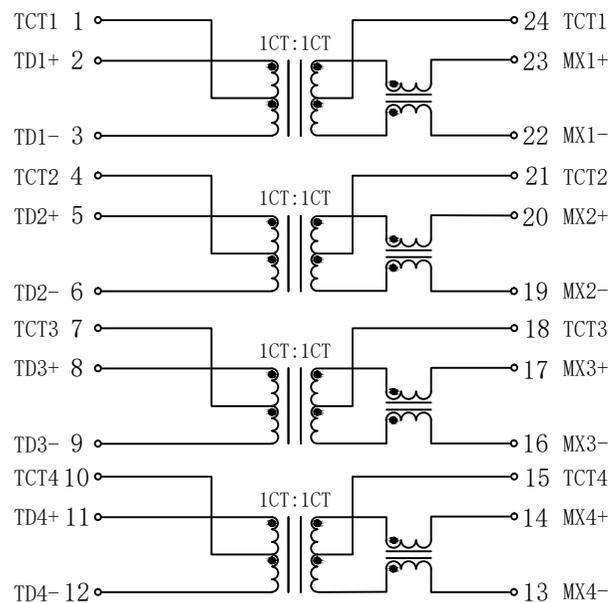
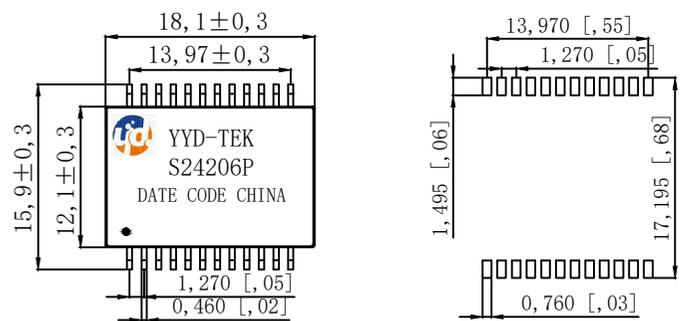


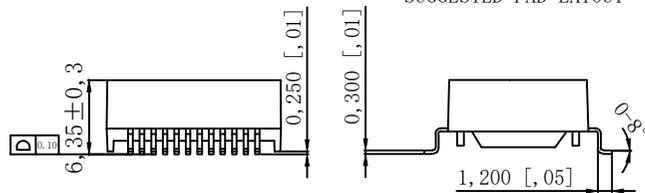
Schematic



Mechanical



SUGGESTED PAD LAYOUT



Units:mm[Inches] Tolerances:xx.x0±0.25[0.010]

0.xx±0.05[0.002]

NO.	REV	DESCRIPTION.	APPD.	Date

ELECTRICAL SPECIFICATONS @25°C

PARAMETER	SPECIFICATIONS
OPERATING TEMPERATURE	0°C to +70°C
STORAGE TEMPERATURE	-25°C TO 125°C
INDUCTANCE (OCL)	350uH MIN @100KHz 100mV With 8mA DC Bias For (CHANNEL 1, 2, 3, 4)
leakage inductance	0.5uH MAX
URNS RATIO	1:1±5%
Cw/w	30pF MAX
D. C. R	1.2Ω MAX
INSERTION LOSS (IL)	-1.0dB MAX@1MHz-100MHz
RETURN LOSS (RL) (Z _{out} =100 OHM)	-18dB MIN@1MHz-30MHz;-16dB MIN@30-60MHz -12dB MIN@60MHz-80MHz;-10dB MIN@100MHz
CROSSTALK	-30dB MIN @1-100MHz
DCMR	-40dB MIN @30MHz;-35dB MIN @60MHz; -30dB MIN @60MHz;
HI-POT	1500Vrms
STANDARD	Designed to meet IEEE802.3at Requirement 720mA MAXIMUM@57VDC (CONTINUOUS)

产品图		 深圳市毅远达电子有限公司 Shenzhen Yi Yuan Da Electronic Co., LTD	
未注公差 TOLERANCE UNLESS OTHERWISE SPECIFIED	0.XX ±0.10 X.XX ±0.25 XX.XX ±0.25 X° ±2°	Description	Designed to meet IEEE802.3at Requirement SMD 24PIN POE++
比例 SCALE	1:1	制图 CHECKED	CAO
张数 SHEET	大小 SIZE	版本 REV.	A0
单位 UNIT	mm inch	角法 PORJ.	核准 APPROVED
日期 DATE	2023-03-28	品名 TITLE	S24206P
日期 DATE		料号 PART NO.	
日期 DATE		图号 DWG NO.	