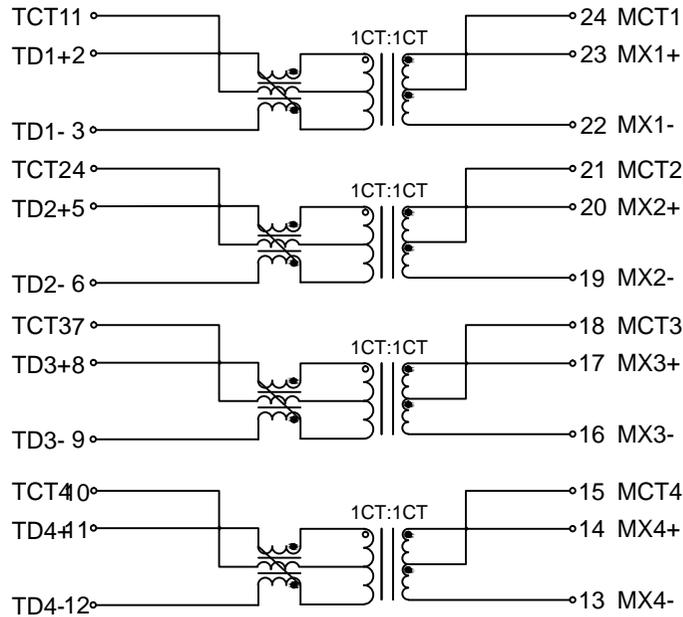
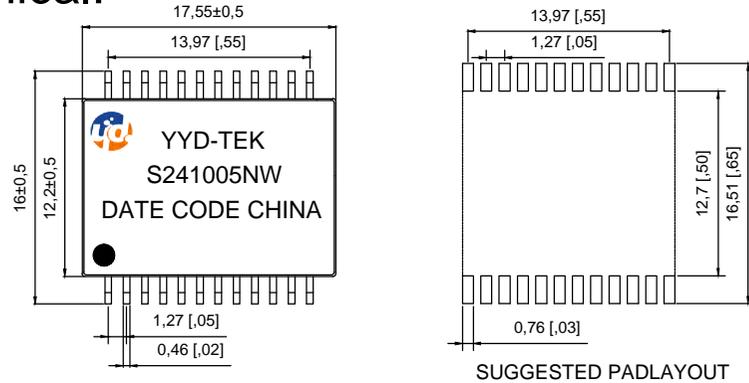


Schematic:



Mechanical:



Units:mm[Inches] Tolerances:xx.x0±0.25[0.010]
0.xx±0.05[0.002]

NO.	REV	DESCRIPTION.	APPD.	Date
1	A0	NEW	Li	2023-02-20

ELECTRICAL SPECIFICATONS @25°C

PARAMETER	SPECIFICATIONS
OPERATING TEMPERATURE	-40°C to +85°C
STORAGE TEMPERATURE	-25°C TO 125°C
INDUCTANCE(OCL)	160uH MIN @100KHz 100mV With 18mA DC Bias For (CHANNEL 1,2,3,4)
leakage inductance	0.5uH MAX
URNS RATIO	1:1±3%
Cw/w	35pF MAX
D.C.R	1.2Ω MAX
INSERTION LOSS(IL)	-0.5dB MAX @1MHz-50MHz; -1.0dB MAX @50MHz-125MHz
RETURN LOSS(RL) (Z out=100 OHM)	-18dB MIN @1MHz-40MHz; -20+15log (f/40) dB Min.40MHz-125MHz
CROSSTALK	-30dB Min @ 1MHz-125MHz
DCMR	-30dB Min @ 1MHz-125MHz
HI-POT	1500Vrms/1mA/1S
STANDARD	Designed to meet IEEE802.3bz/bt Requiement Suitable for PoE application with 720mA current capability

产品图		深圳市毅远达电子有限公司 Shenzhen Yi Yuan Da Electronic Co., LTD	
未注公差 TOLERANCE UNLESS OTHERWISE SPECIFIED	0. XX ±0.10 X. XX ±0.25 XX. XX ±0.25 X° ±2°	Description	Designed to meet IEEE802.3bz/bt Requiement 25PIN SMD 2.5G PROFILE PACKAGE
比例 SCALE	1:1	版本 REV.	A0
张数 SHEET	3/4	制图 CHECKED	li
单位 UNIT	mm inch	角法 PORJ.	核准 APPROCED
日期 DATE	2023-02-20	品名 TITLE	S241005NW
日期 DATE		料号 PART NO.	
日期 DATE		图号 DWG NO.	