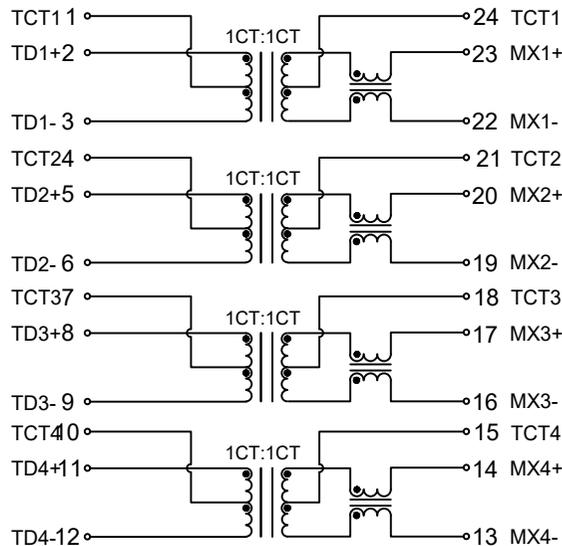
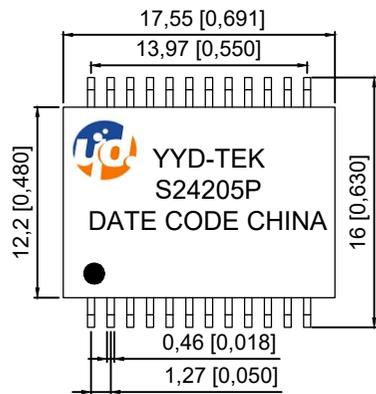


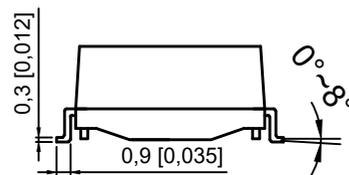
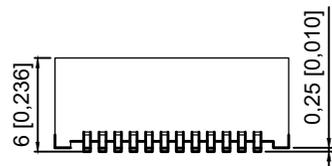
Schematic



Mechanical



SUGGESTED PADLAYOUT



NO.		REV	DESCRIPTION.	APPD.	Date
NO.		A0	NEW		2023-12-30

ELECTRICAL SPECIFICATIONS@25°C

PARAMETER	SPECIFICATIONS
OPERATING TEMPERATURE	0°C to +70°C
STORAGE TEMPERATURE	-25°C TO 125°C
INDUCTANCE(OCL)	350uH MIN @100KHz 100mV With 8mA DC Bias For (CHANNEL 1,2,3,4)
leakage inductance	0.5uH MAX
TURNS RATIO	1:1±5%
Cw/w	35pF MAX
D.C.R	1.2Ω MAX
INSERTION LOSS(IL)	1.0dB MAX@1MHz-100MHz
RETURN LOSS(RL) (Z out=100 OHM)	-18dB MIN@0.1MHz-30MHz;-16dB MIN@40-50MHz -12dB MIN@60MHz-80MHz;-10dB MIN@100MHz
CROSSTALK	-30dB MIN@1MHz-100MHz;
DCMR	-30dB MIN@1MHz-100MHz;
HI-POT	1500Vrms/1mA/1S
STANDARD	Designed to meet IEEE802.3at Requirement Suitable for POE application with 720mA current capability

产品图			深圳市毅远达电子有限公司 Shenzhen Yi Yuan Da Electronic Co., LTD						
未注公差 TOLERANCE UNLESS OTHERWISE SPECIFIED	0. XX	±0.10	Description Designed to meet IEEE802.3at Requirement 24PIN SMD POE 720mA PROFILE PACKAGE						
	X. XX	±0.25		比例 SCALE	1:1				
	XX. XX	±0.25		张数 SHEET	5/5				
	x'	±2'	版本 REV.	A0	日期 DATE	2023/05/13	品名 TITLE	S24205P	
比例 SCALE	1:1	版本 REV.	A0	制图 CHECKED	CA0	日期 DATE	2023/05/13	料号 PART NO.	
张数 SHEET	5/5	大小 SIZE	A4	校对 CHECKED		日期 DATE	2023/05/13	料号 PART NO.	
单位 UNIT	mm Inch	角法 POR.]	☉	核准 APPROVED		日期 DATE		图号 DWG NO.	