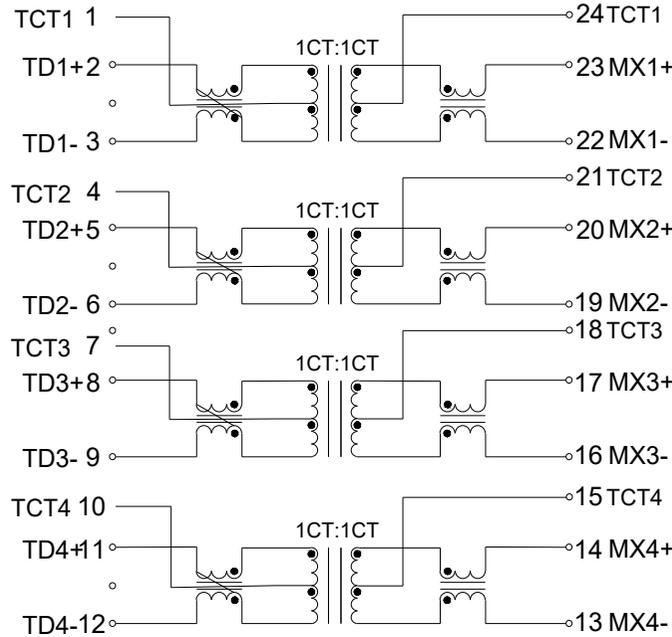
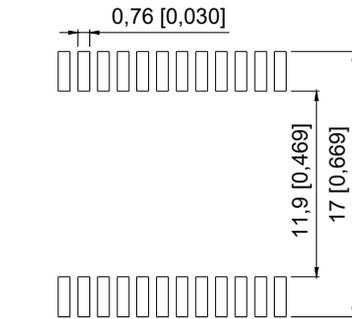
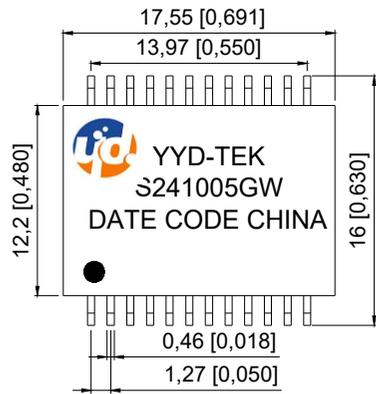


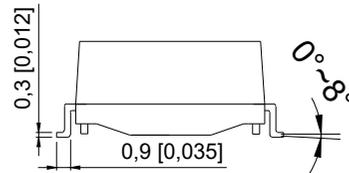
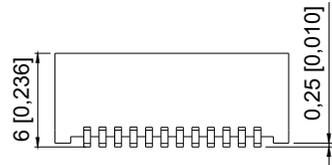
Schematic



Mechanical



SUGGESTED PADLAYOUT



REV	DESCRIPTION.	APPD.	Date

ELECTRICAL SPECIFICATONS@25°C

PARAMETER	SPECIFICATIONS
OPERATING TEMPERATURE	-40°C to +85°C
STORAGE TEMPERATURE	-40°C TO 85°C
INDUCTANCE(OCL)	180uH MIN @100KHz 100mV With0mA DC Bias For (CHANNEL 1,2,3,4)
leakage inductance	0.5uH MAX
TURNS RATIO	1:1±5%
Cw/w	35pF MAX
D.C.R	1.2Ω MAX
INSERTION LOSS(IL)	-1.0dB MAX@1MHz-100MHz -2.0dB MAX@100MHz-150MHz
RETURN LOSS(RL) (Z out=100 OHM)	-16dB MIN@0.1MHz-40MHz;-14.2dB MIN@40-60MHz -12dB MIN@60MHz-150MHz;-10dB MIN@150-250MHz
CROSSTALK	-38dB MIN@1MHz-100MHz; -30dB MIN@100MHz-250MHz
DCMR	-38dB MIN@1MHz-100MHz; -25dB MIN@100MHz-250MHz;
HI-POT	3000Vrms/1mA/1S
STANDARD	Designed to meet IEEE802.3at Requiement Suitable for POE application with 720mA current capability

深圳市毅远达电子有限公司
Shenzhen Yi Yuan Da Electronic Co., LTD

FILE NO :	TITLE 品名	S241005G			UNIT 單位	mm inch
TOLERANCE(公差): 0.000 mm ± 0.10 mm 0.00 mm ± 0.25 mm 0.0 mm ± 0.30 mm	PART NO. 料号				SCALE 比例	1 : 1
	DRAWN BY 制图	CAO	DATE 日期	20240706	REV. 版本	A0
	CHECKED BY 审核		DATE 日期			